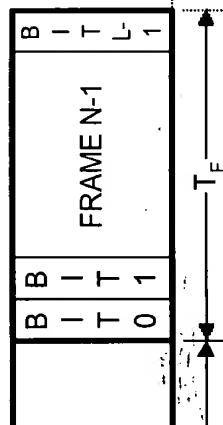
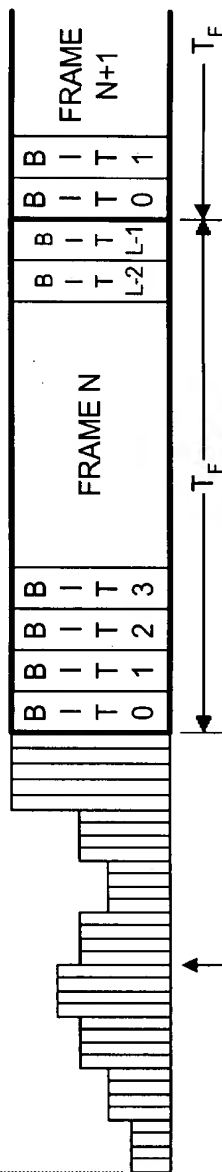


FIG. 25

SOURCE BS



TARGET BS



Differentially Encoded  
Power Control Bits

MS

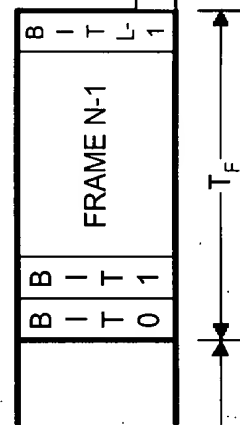


FIG. 26

FIG. 1

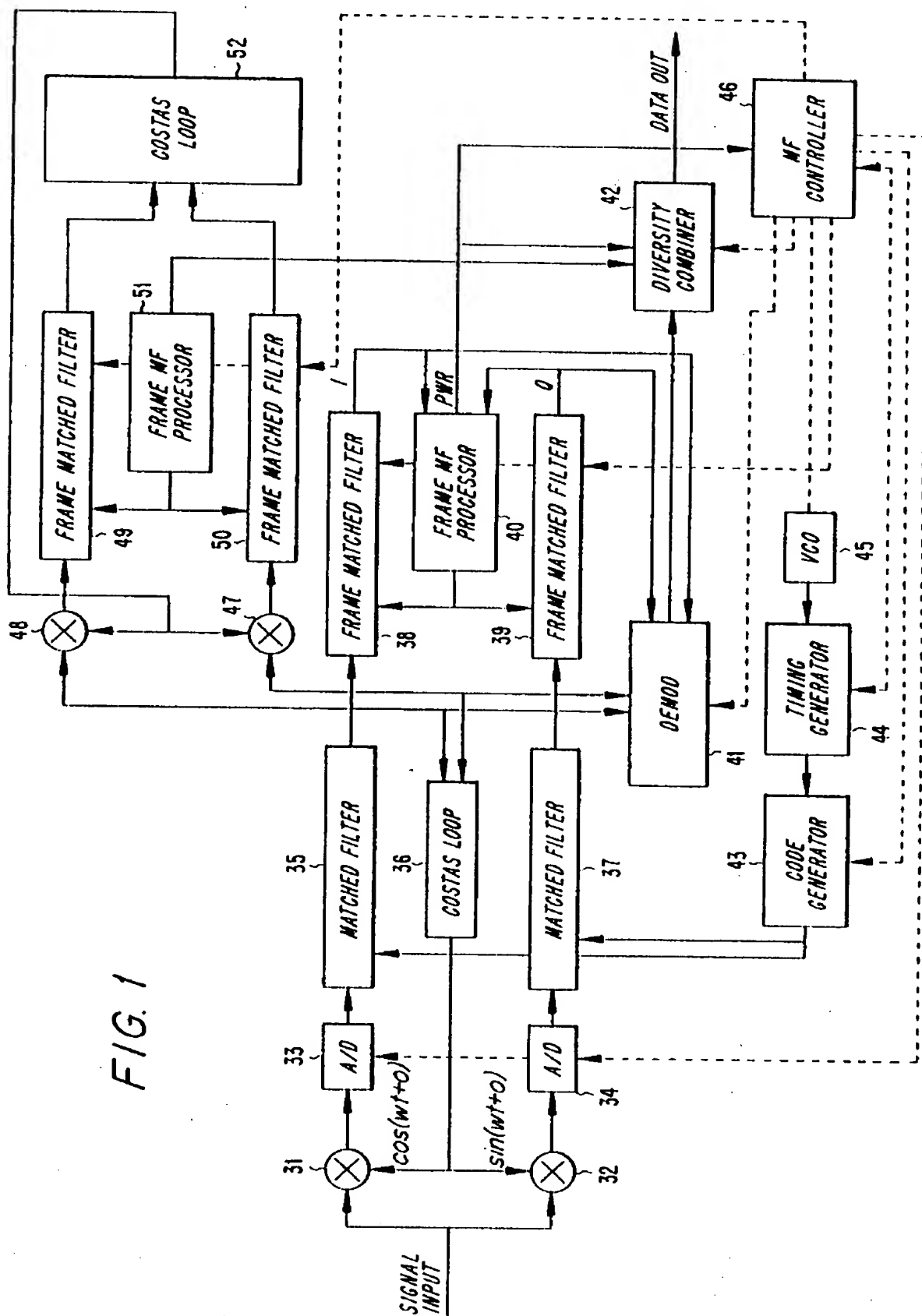


FIG. 2

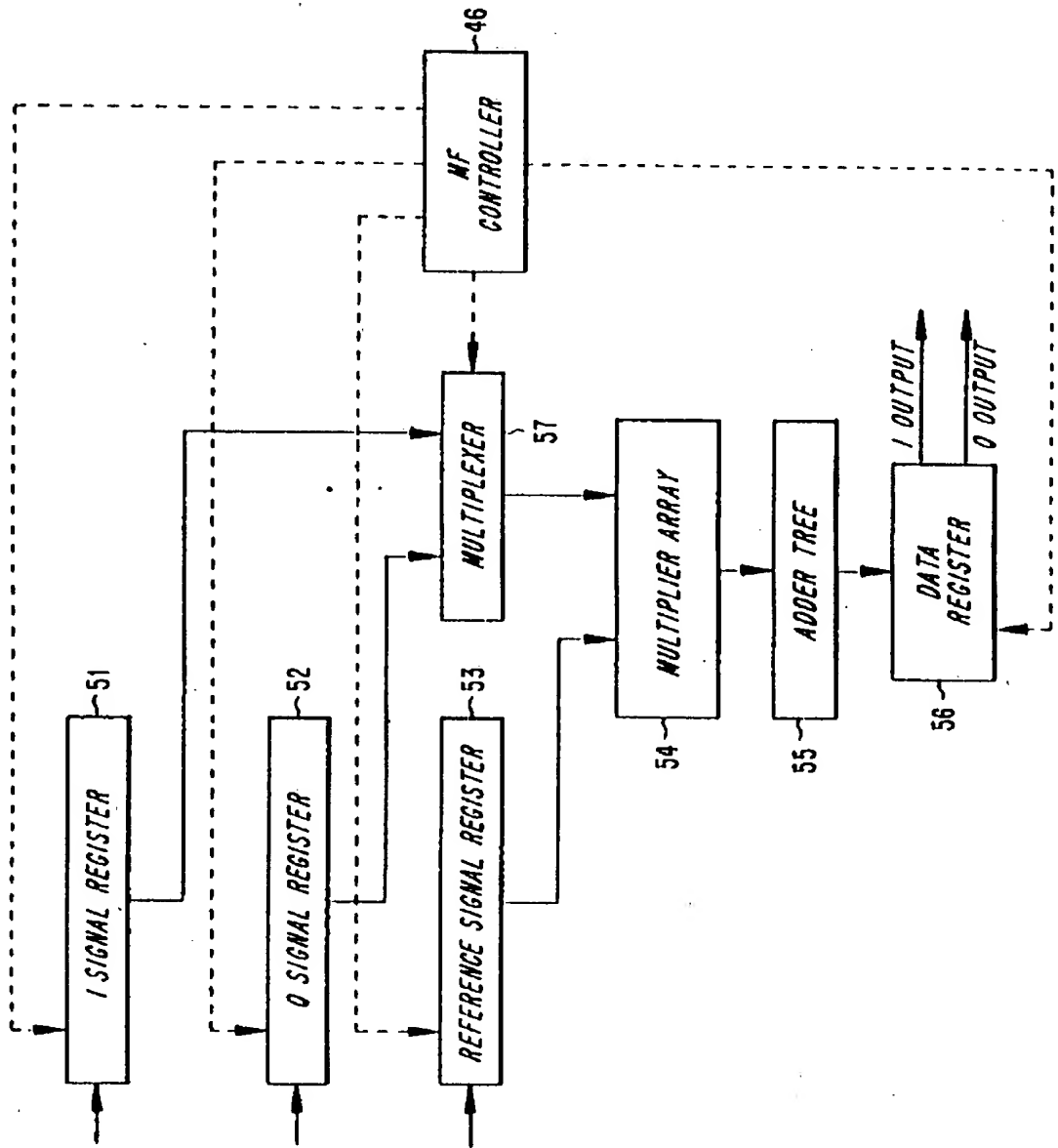


FIG. 3

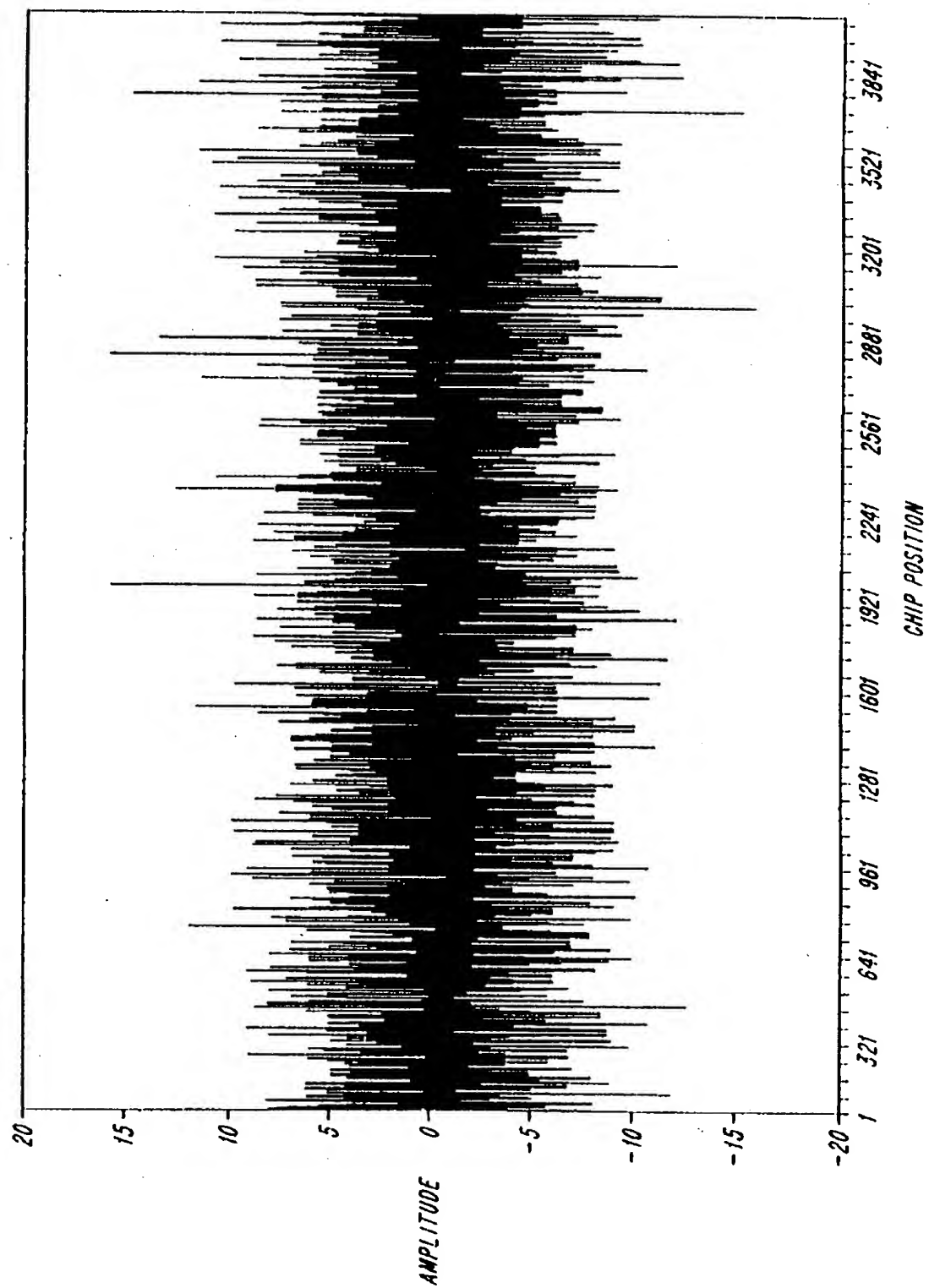


FIG. 4

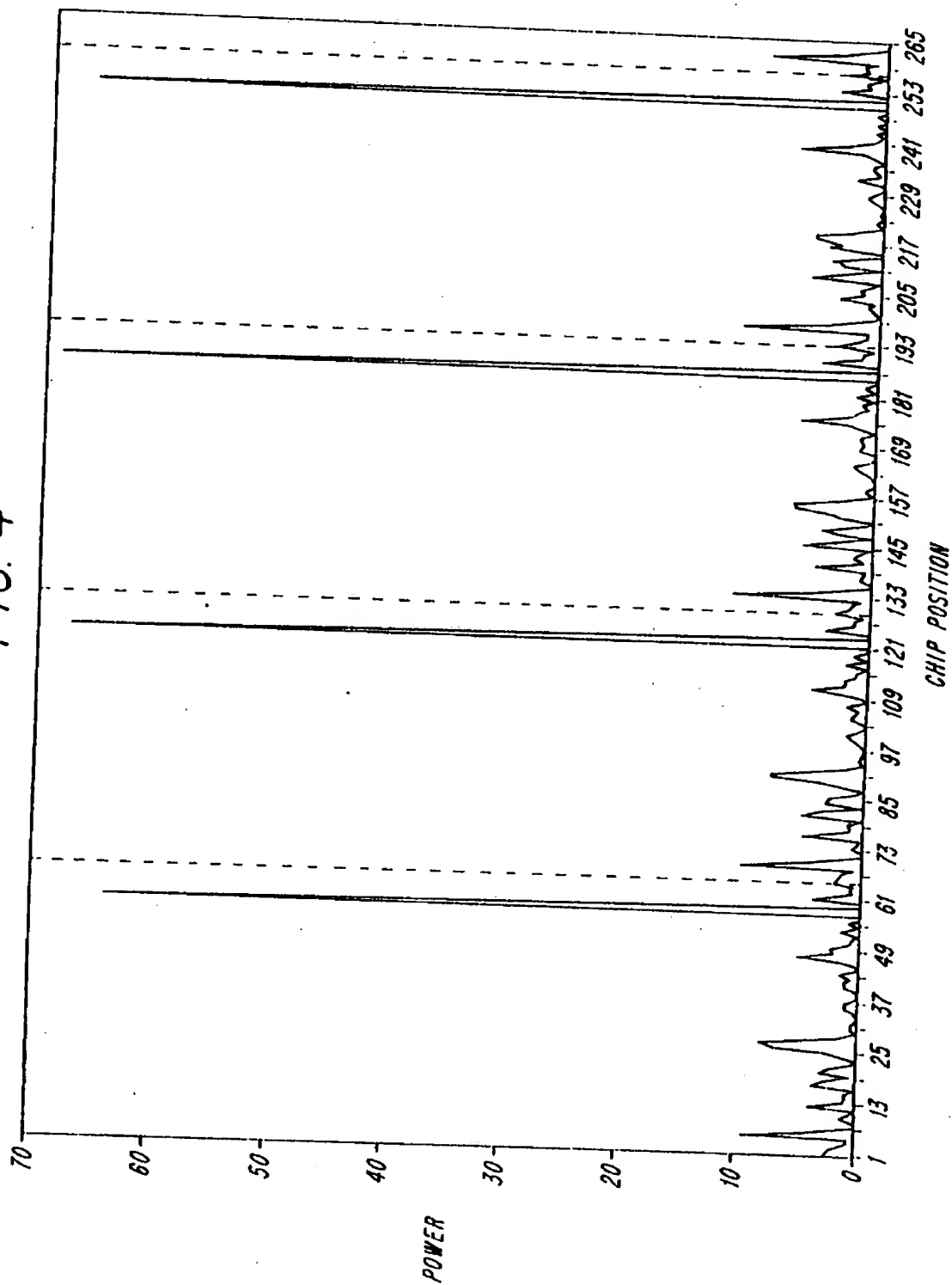


FIG. 5

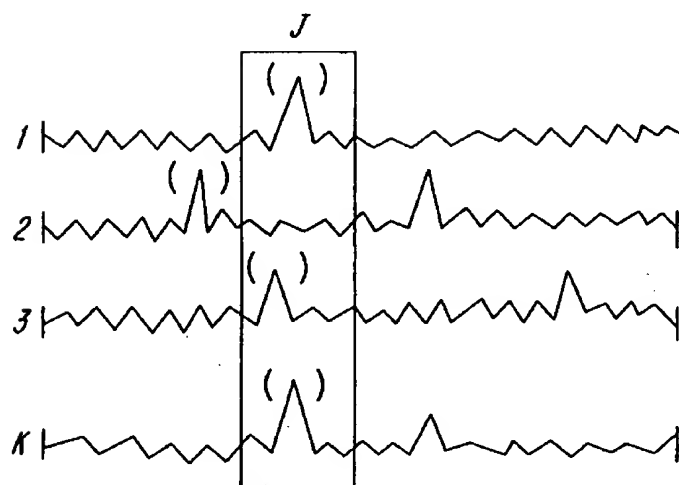


FIG. 6

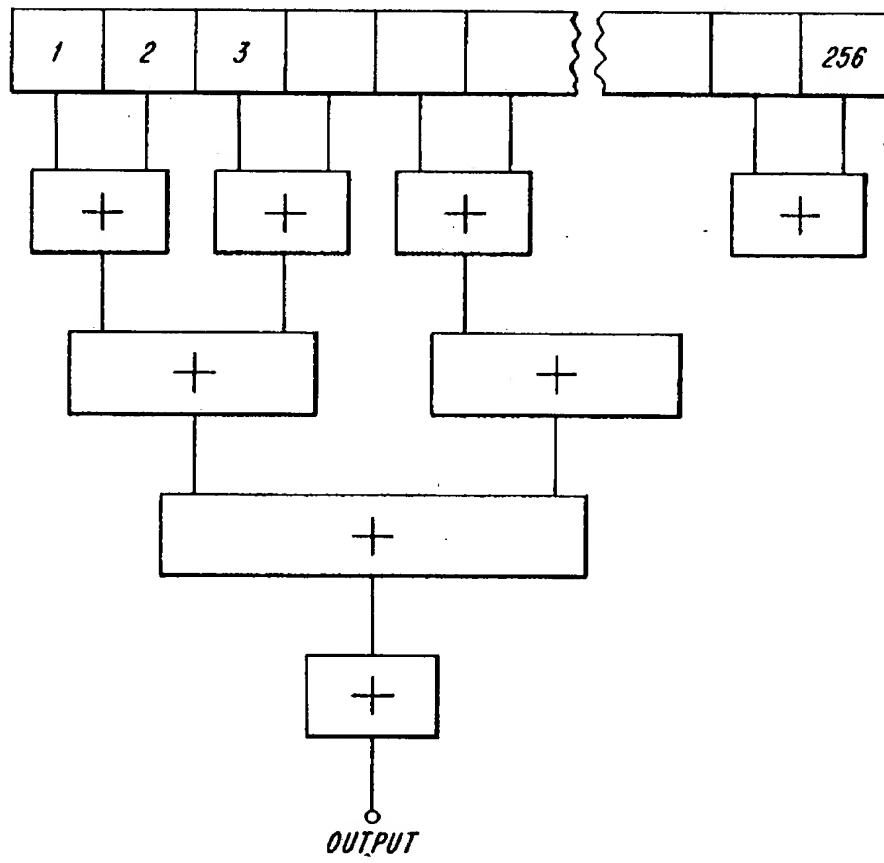


FIG. 7

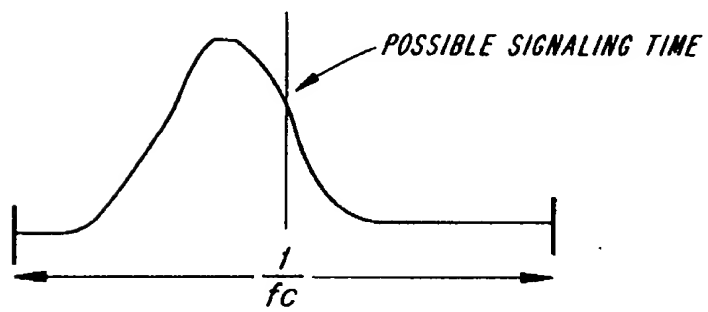




FIG. 8

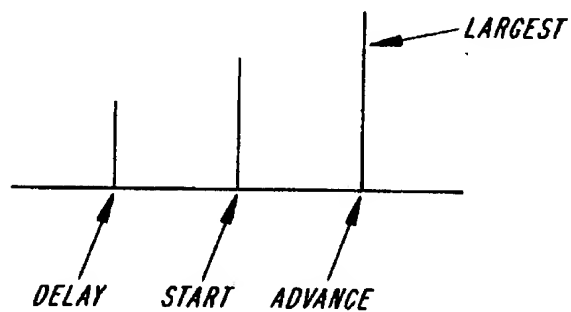


FIG. 9

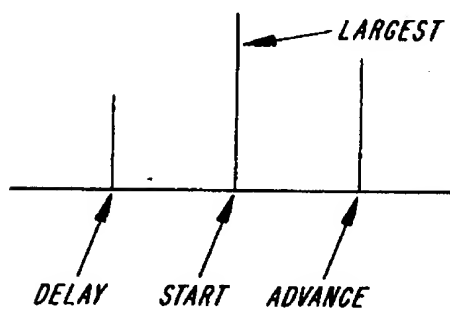


FIG. 10

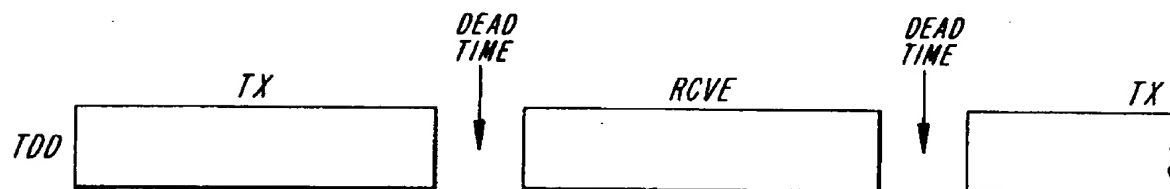


FIG. 11

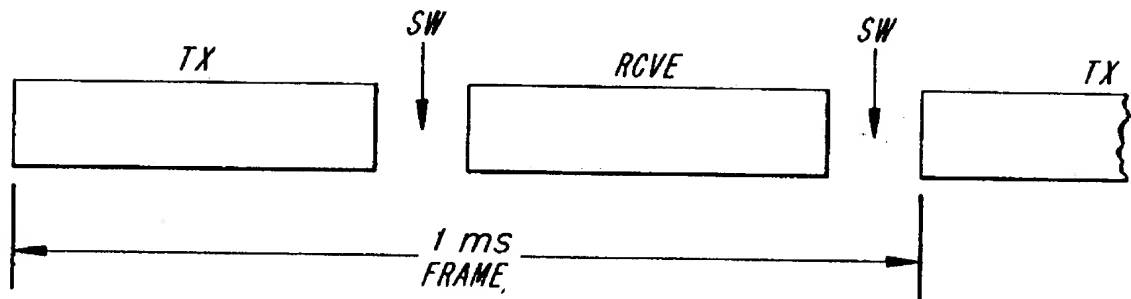


FIG. 12

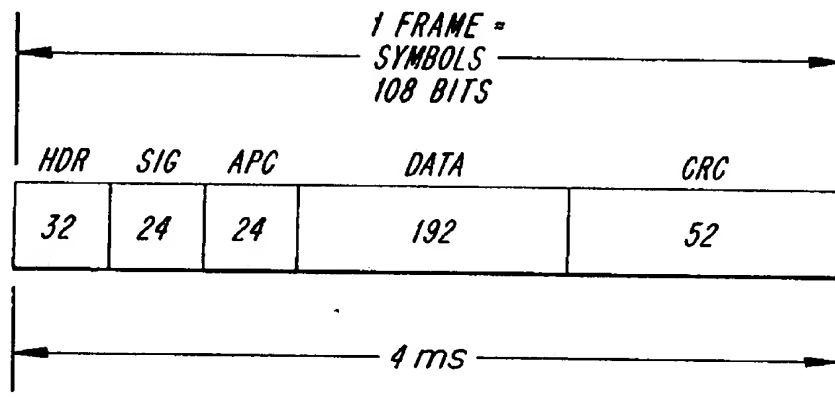
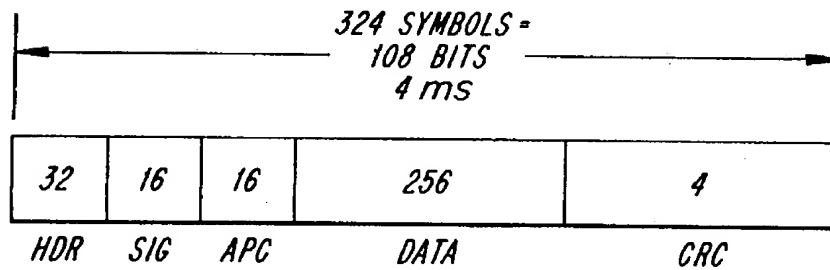
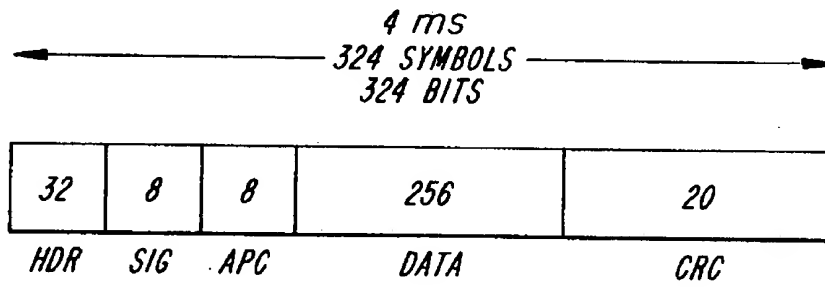
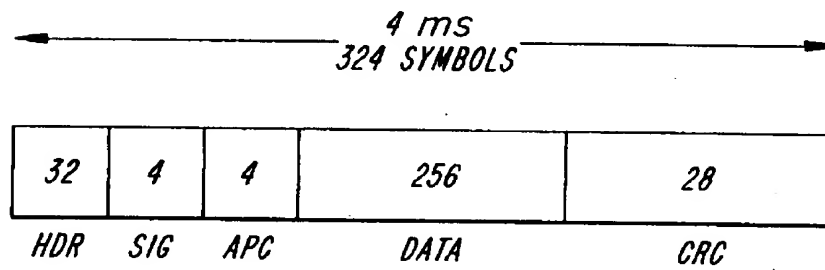


FIG. 13

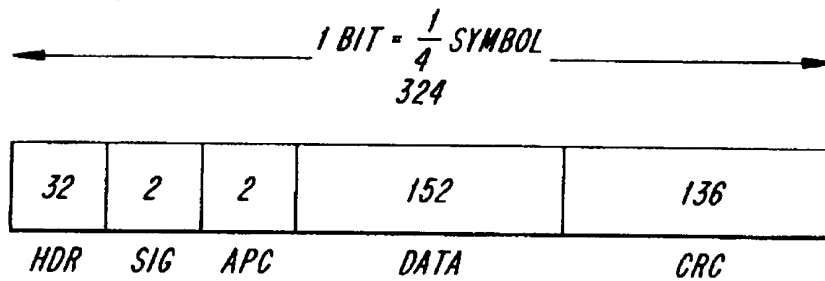




*FIG. 14*



*FIG. 15*



*FIG. 16*

$$1 \text{ BIT} = \frac{1}{8} \text{ SYMBOL}$$

32	1	1	152	138
<i>HDR</i>	<i>SIG</i>	<i>APC</i>	<i>DATA</i>	<i>CRC</i>

*FIG. 17*

$$1 \text{ BIT} = \frac{1}{8} \text{ SYMBOL}$$

32	1	1	192	98
<i>HDR</i>	<i>SIG</i>	<i>APC</i>	<i>DATA</i>	<i>CRC</i>

*FIG. 18*

$$1 \text{ BIT} = \frac{1}{16} \text{ SYMBOL}$$

32	$\frac{1}{2}$	$\frac{1}{2}$	192	99
<i>HDR</i>	<i>SIG</i>	<i>APC</i>	<i>DATA</i>	<i>CRC</i>

*FIG. 19*

$$1 \text{ BIT} = \frac{1}{32} \text{ SYMBOL}$$

$\longleftrightarrow$ 324 $\longrightarrow$				
32	$\frac{1}{4}$	$\frac{1}{4}$	256	35.5
<i>HDR</i>	<i>SIG</i>	<i>APC</i>	<i>DATA</i>	<i>CRC</i>

*FIG. 20*

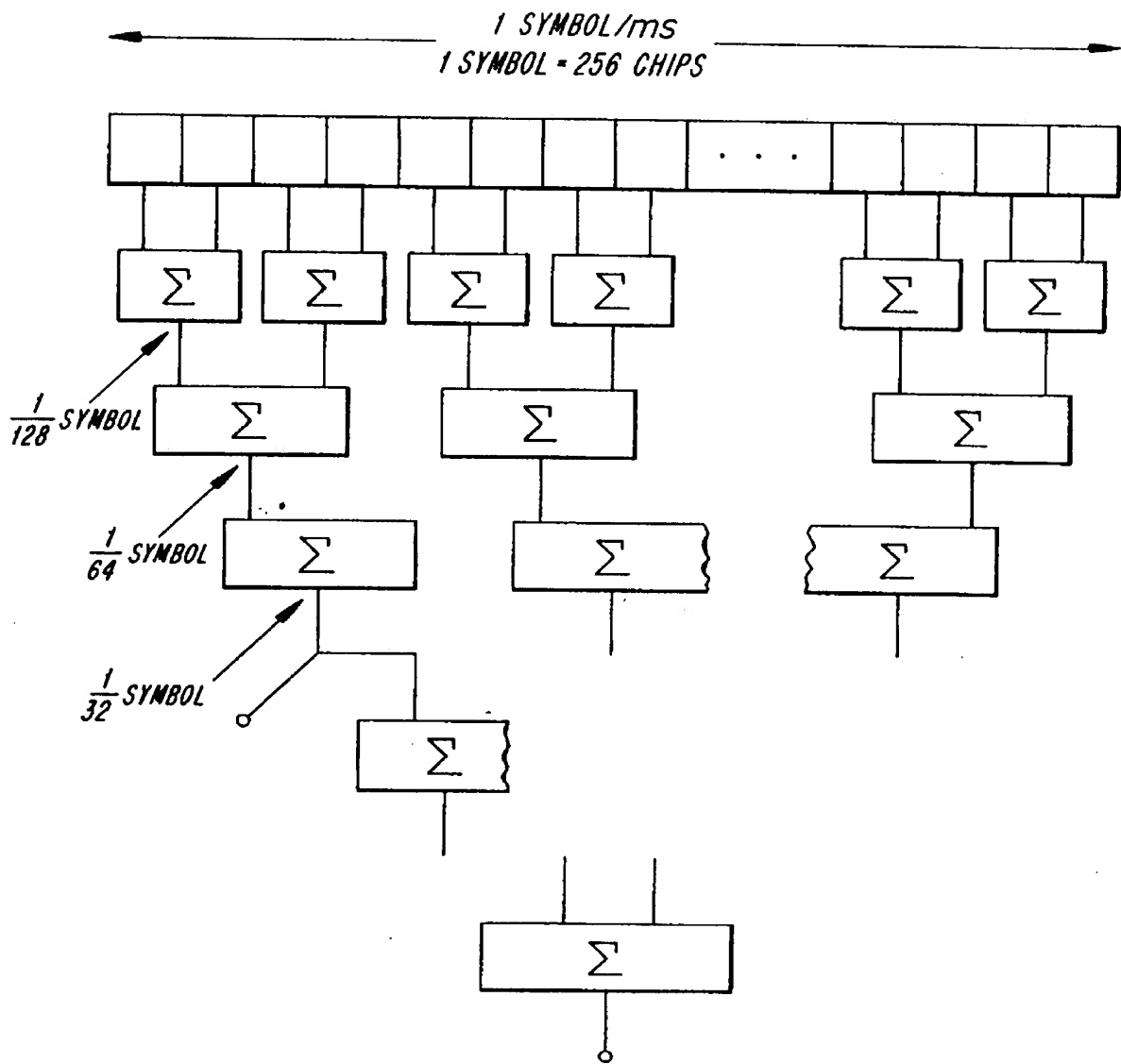
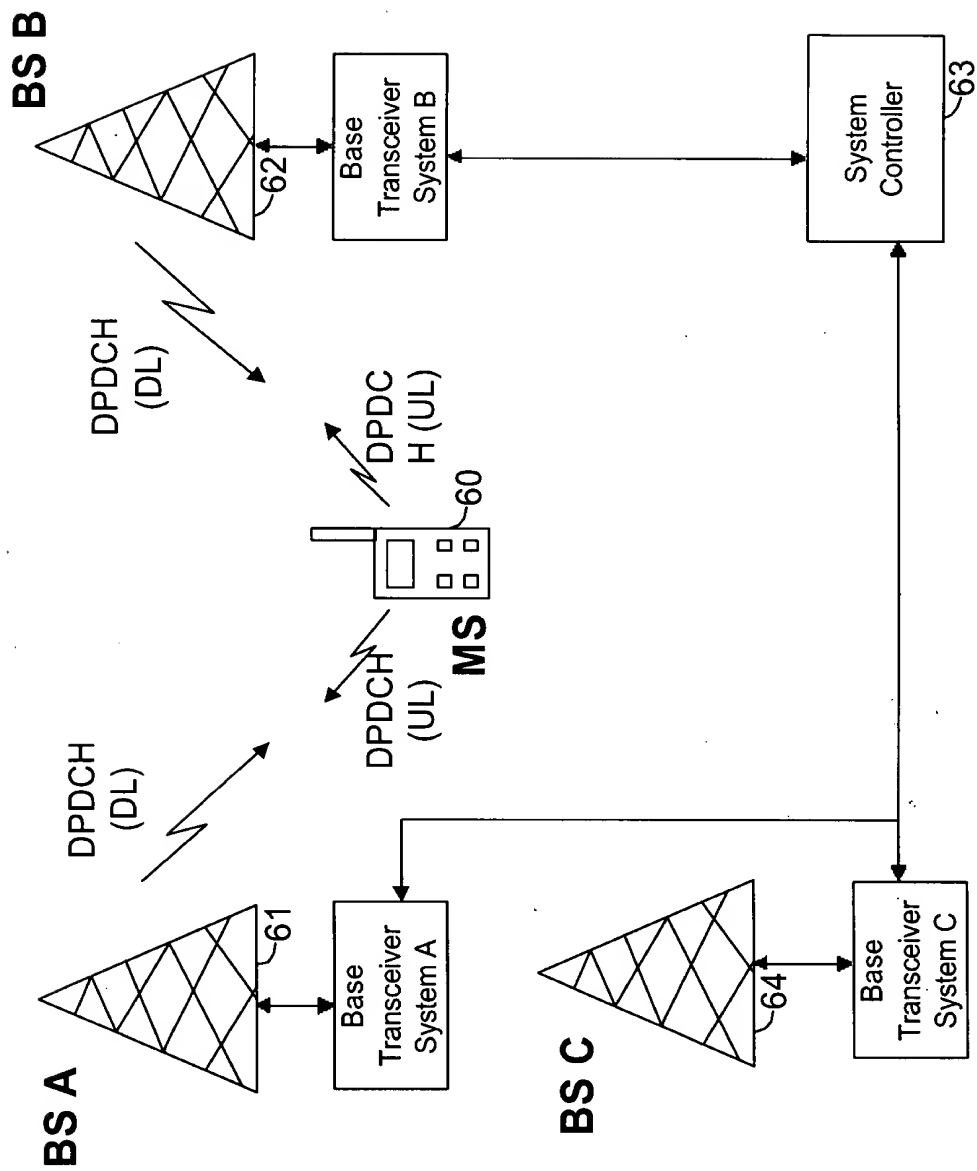
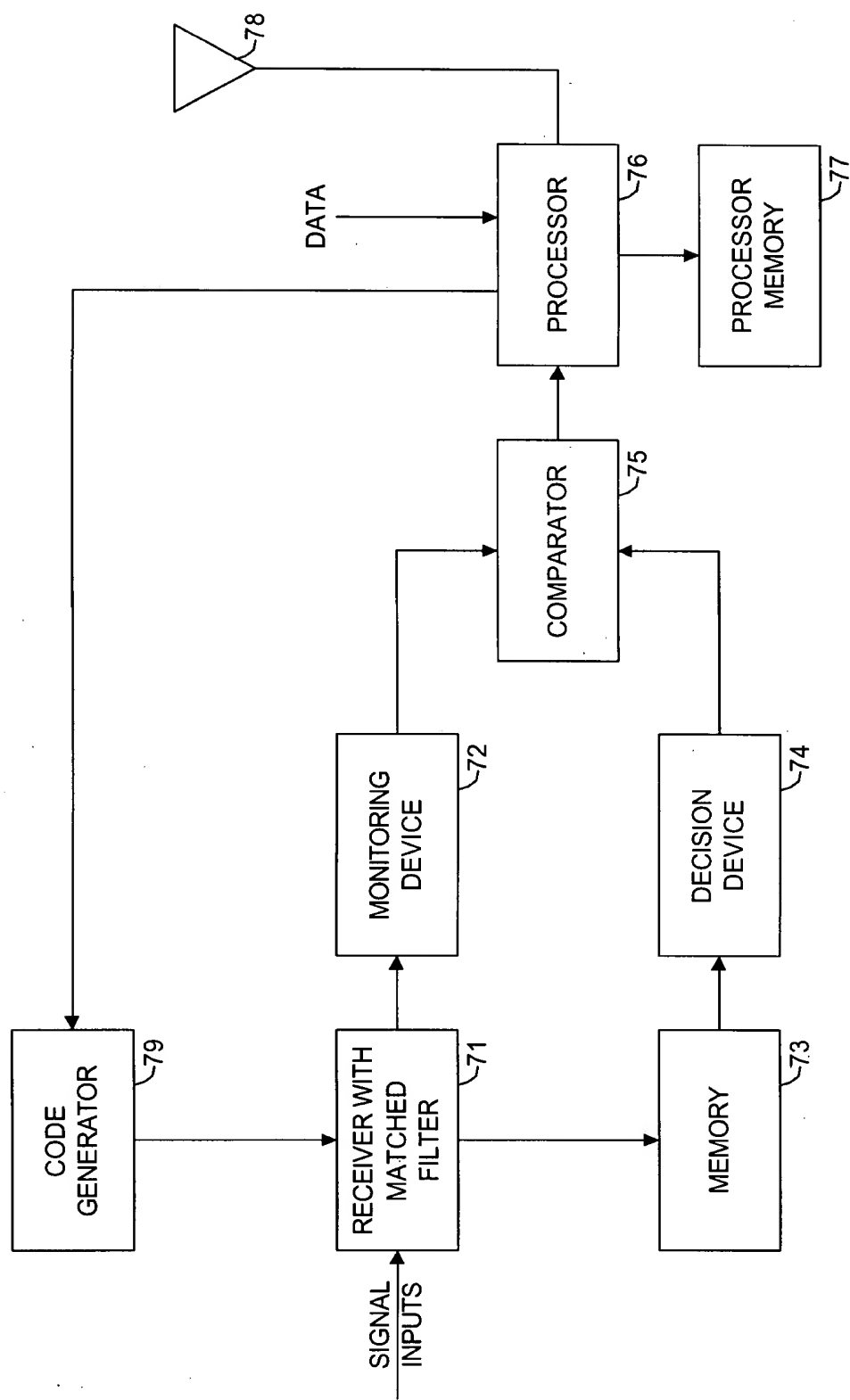


FIG. 21



**FIG. 22**



**FIG. 23**

